

STATE BOARD OF TECHNICAL EDUCATION AND TRAINING  
BOARD OF EXAMINATIONS, CHENNAI 600 025.

**TIME TABLE – APRIL 2014**

***DIPLOMA IN ELECTRONICS AND COMMUNICATION ENGINEERING (1040)***

Date and Day	Time	Subject Code	Subjects as per the Scheme of Examination	Code No.
<b>( L-Scheme )</b>				
<b>III SEMESTER</b>				
11.4.2014 Friday	2.15 pm – 5.15 pm	24031	Electronic Devices and Circuits	
15.4.2014 Tuesday	2.15 pm – 5.15 pm	25233	'C' Programming #	
17.4.2014 Thursday	2.15 pm – 5.15 pm	24032	Electrical Circuits and Instrumentation	
<b>IV SEMESTER</b>				
10.4.2014 Thursday	2.15 pm – 5.15 pm	24041	Industrial Electronics	
12.4.2014 Saturday	2.15 pm – 5.15 pm	24042	Communication Engineering	
16.4.2014 Wednesday	2.15 pm – 5.15 pm	24043	Digital Electronics	
19.4.2014 Saturday	2.15 pm – 5.15 pm	24044	Linear Integrated Circuits	
<b>V SEMESTER</b>				
11.4.2014 Friday	9.30 am – 12.30 pm	24051	Advanced Communication Systems	
15.4.2014 Tuesday	9.30 am – 12.30 pm	24052	Microcontroller	
17.4.2014 Thursday	9.30 am – 12.30 pm	24053	Very Large Scale Integration	
21.4.2014 Monday	9.30 am – 12.30 pm	24071	<b>Elective Theory – I</b> (a) Digital Communication	
		23072	(b) Programmable Logic Controller @	
<b>VI SEMESTER</b>				
10.4.2014 Thursday	9.30 am – 12.30 pm	24061	Embedded Systems	
12.4.2014 Saturday	9.30 am – 12.30 pm	24081	<b>Elective Theory-II</b> (a) Television Engineering	
		23082	(b) Biomedical Instrumentation @	
		24083	(c) Electronic System Design	
16.4.2014 Wednesday	9.30 am – 12.30 pm	24682	Computer Hardware and Networks \$	
<b>( K-Scheme )</b>				
<b>V SEMESTER</b>				
19.4.2014 Saturday	9.30 am – 12.30 pm	24763	<b>Elective Theory – I</b> (c) Robotics and Auto Electronics **	
		14071	(a) Digital Signal Processing	
<b>VI SEMESTER</b>				
21.4.2014 Monday	9.30 am – 12.30 pm	14081	<b>Elective Theory – II</b> (a) Digital Image Processing	

# Common with Computer Engineering (L – Scheme)

\$ Common with Information Tech (L-Scheme)

@ Common with EEE (L-Scheme)

\*\* Common with Electronics (R) (L-Scheme)